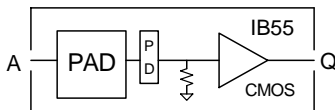


IB55 is a CMOS-level input buffer pad with pull-down.

Truth Table

A	Q
L	L
H	H
high imp.	L



Capacitance

	Ci (pF)
A	1.002

Area

71.61 mils²

Power

6.94 μ W/MHz

Delay [ns] = tpd.. = f(SL, L) with SL = Input Slope [ns] ; L = Output Load [pF]

Output Slope [ns] = op_sl.. = f(L) with L = Output Load [pF]

AC Characteristics : T_j = 25°C VDD = 3.3V Typical Process

AC Characteristics

Characteristics	Symbol	SL = 0.1			SL = 2.0		
		L = 0.2	L = 1.4	L = 2.0	L = 0.2	L = 1.4	L = 2.0
Delay A to Q	tpdar	0.45	1.07	1.35	0.66	1.27	1.57
	tpdaf	0.48	0.95	1.18	0.83	1.30	1.52
Output Slope A to Q	op_slar	0.61	2.43	3.40	0.63	2.43	3.32
	op_slaf	0.45	1.66	2.22	0.50	1.62	2.21